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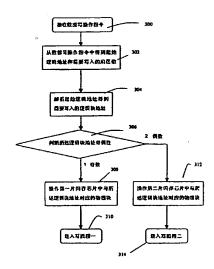
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**所引用双字母代码和其它缩写符号,请参考刊登在每期** PCT公报期刊起始的"代码及缩写符号简要说明"。

- (54) Title: DATA WRITE-IN METHOD FOR FLASH MEMORY
- (54) 发明名称: 闪存介质数据写入方法



- RECEIVE THE DATA WRITE IN OPERATING INSTRUCTION OBTAIN THE START LOCICAL ADDRESS AND THE NUMBER OF THE SECTOR THAT SHOULD BE WROTE-IN ACCORDING TO THE DATA WRITE IN OPERATING INSTRUCTION
- ANALYSE THE START LOGICAL ADDRESS TO OBTAIN THE LOGICAL ADDRESS THAT SHOULD BE WROTE-IN
- JUDGE THE PARTIY OF SAID LOGICAL SLOCK ADDRESS
- OPERATE ON THE PHYSICAL BLOCK WHICH CORRESPONDS TO BAID LOGICAL BLOCK ADDRESS AND IN THE FIRST FLAS
- GO TO THE WRITING PROCESS ONE
- OPERATE ON THE PHYSICAL BLOCK WHICH CORRESPONDING TO SAD LOGICAL BLOCK ADDRESS AND IN THE SECOND FLASH CHIP
- 314 GO TO THE WRITING PROCESS TWO

(57) Abstract: The present invention provide a data write-in method of flash memory for writing data into two or more flash chips, the method comprise: priority corresponds the physical blocks in the two flash chips to the odd logical block address and the even logical block address respectively; analyse the logical block address which corresponds to the write-in operation from the data write-in instruction; judge the parity of said logical block address and select the corresponding flash chip according to the result; operate the flash chip, detect whether or not the other flash chip needs to be programmed or crased after direct the instruction to program or erase the flash chip, when the other flash chip needs to be programmed or erased, then direct the program or erase instruction to the other flash chip. Using the method of the present invention can program and erase two flash chips simultaneously, thereby increase the data write-in speed greatly.

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